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System on Chip Integration (Special Section in IEEE Transactions on VLSI Systems)

Target Publication Date: April – May '07

Driven by the rapid growth of the Internet, communication technologies, pervasive computing, and wireless and portable consumer electronics, *Systems-On-Chip* (SOC) have become a dominant issue in today's semiconductor industry. SOCs have created new challenges in Design Methods, Design Tools, Design Automation, Manufacturing, Technology, and Test. The **System on Chip Integration** special section provides a forum for sharing advances in SOC technologies and applications. The special section will also highlight selected work presented at the September 2006 IEEE-SOCC conference (<http://www.ieee-socc.org>).

SUBMISSION OF MANUSCRIPTS

The authors must submit full papers as Adobe PDF file at the IEEE manuscript central web site for the IEEE Transactions on VLSI Systems (TVLSI) accessible through <http://www.csee.usf.edu/tvlsi/> and the papers should strictly follow the submission guidelines of TVLSI in terms of paper length, format etc. The information for authors can be found at the website. It should be indicated during submission in the space for *note to Editor-In-Chief* that the submission is for the special section.

TECHNICAL SCOPE

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